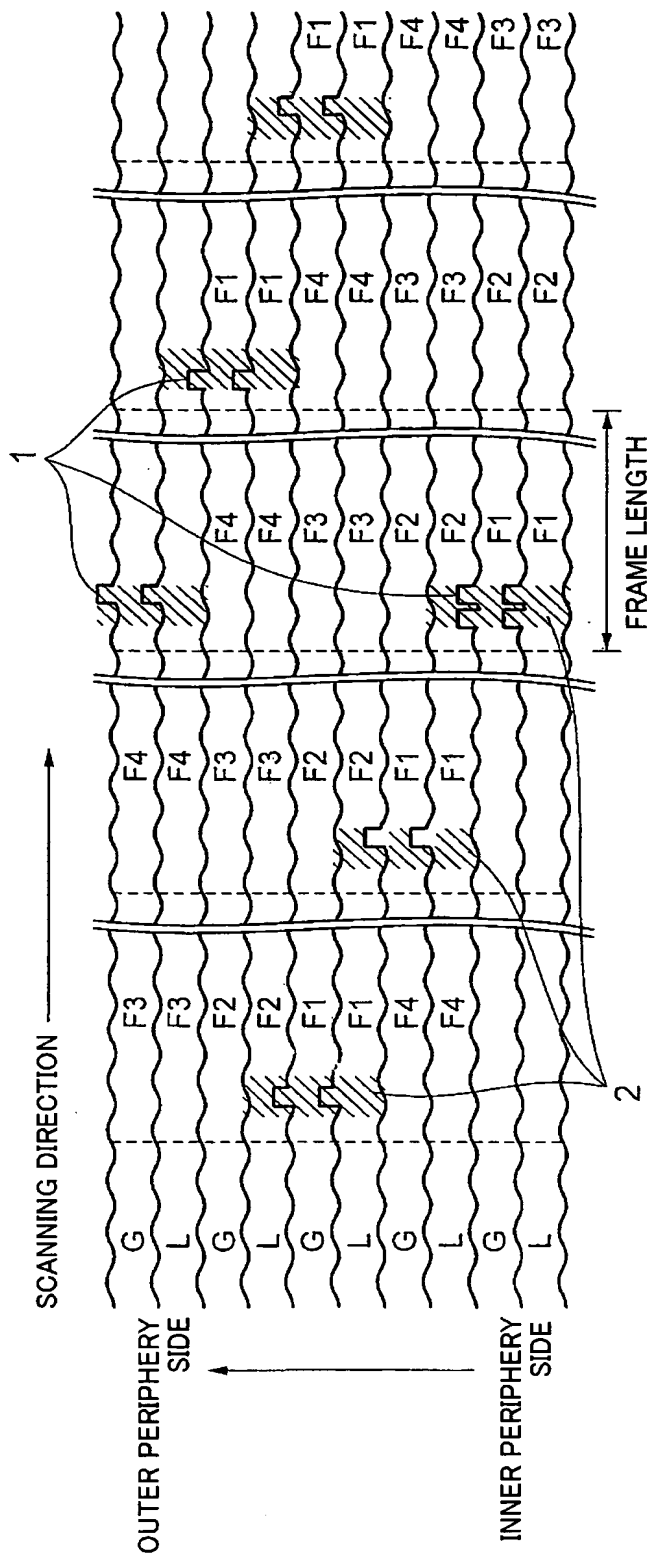


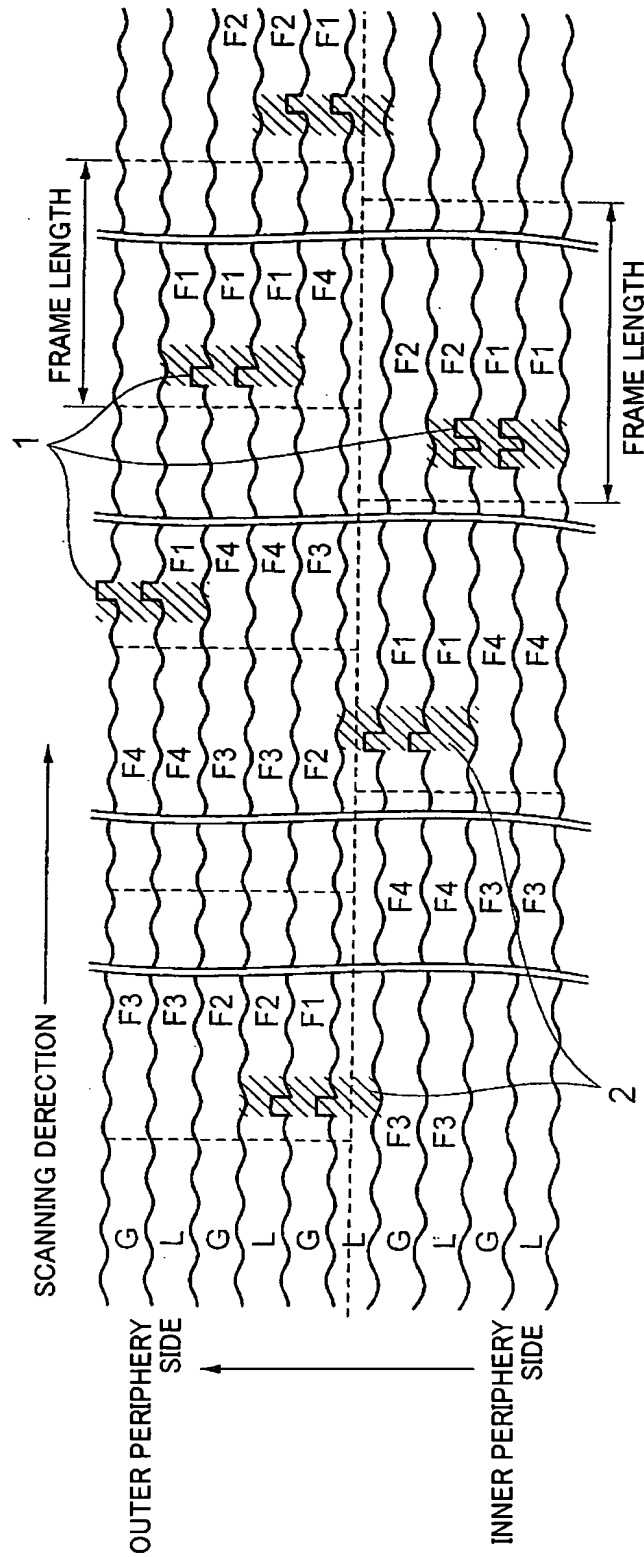
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FIG. 1



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FIG. 2

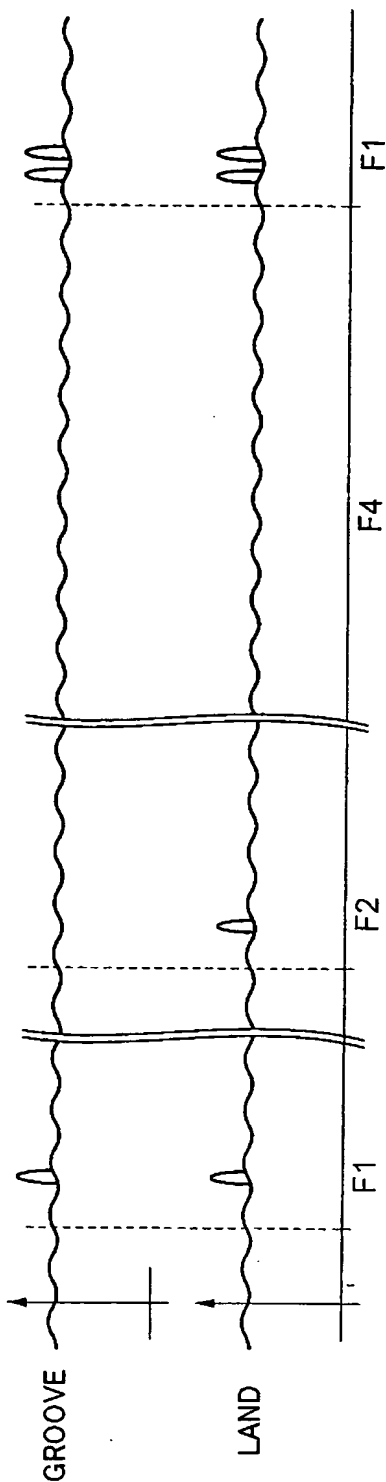


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FIG. 3



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FIG. 4

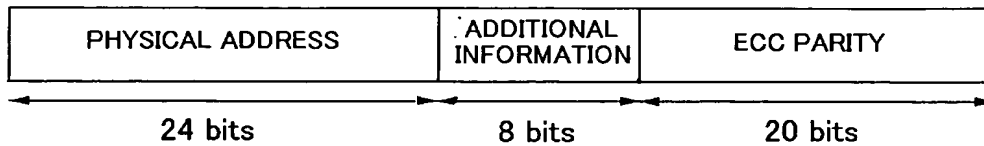
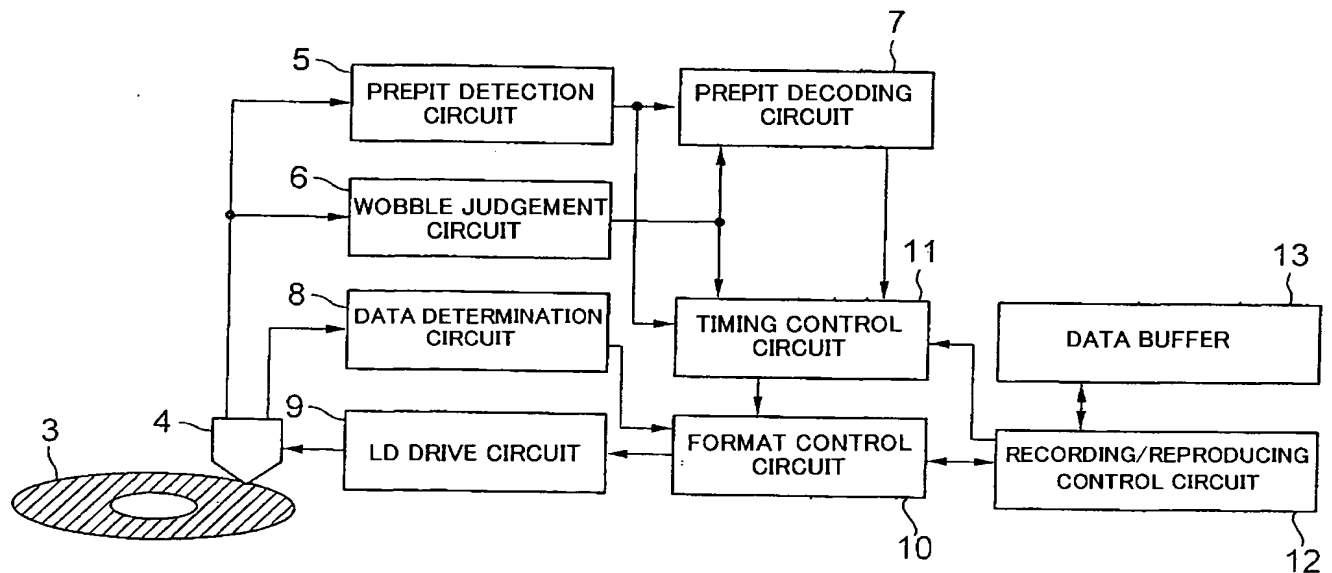
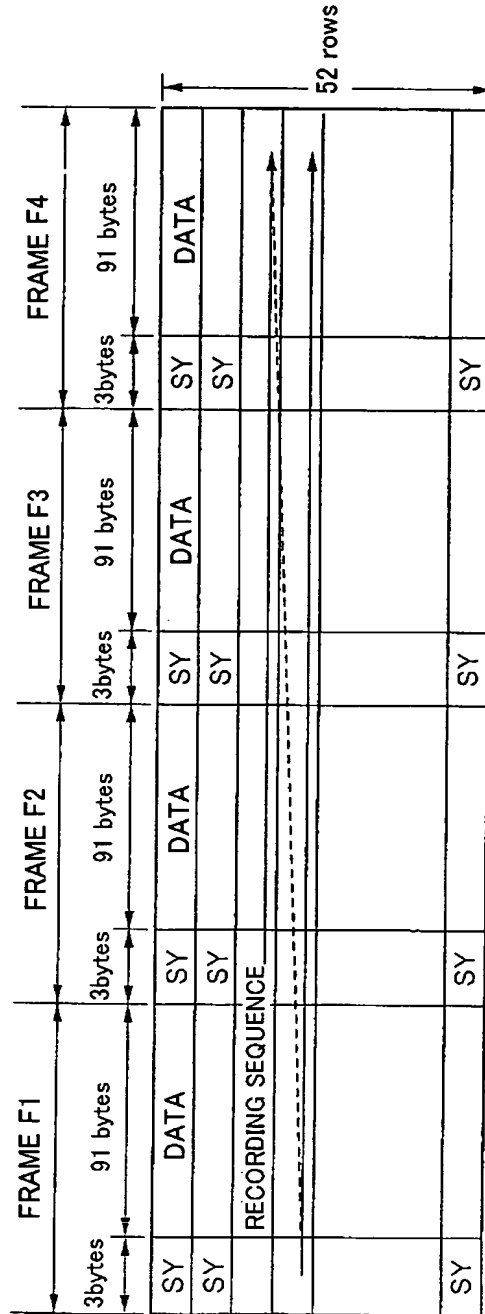


FIG. 5



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FIG. 6



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FIG. 7

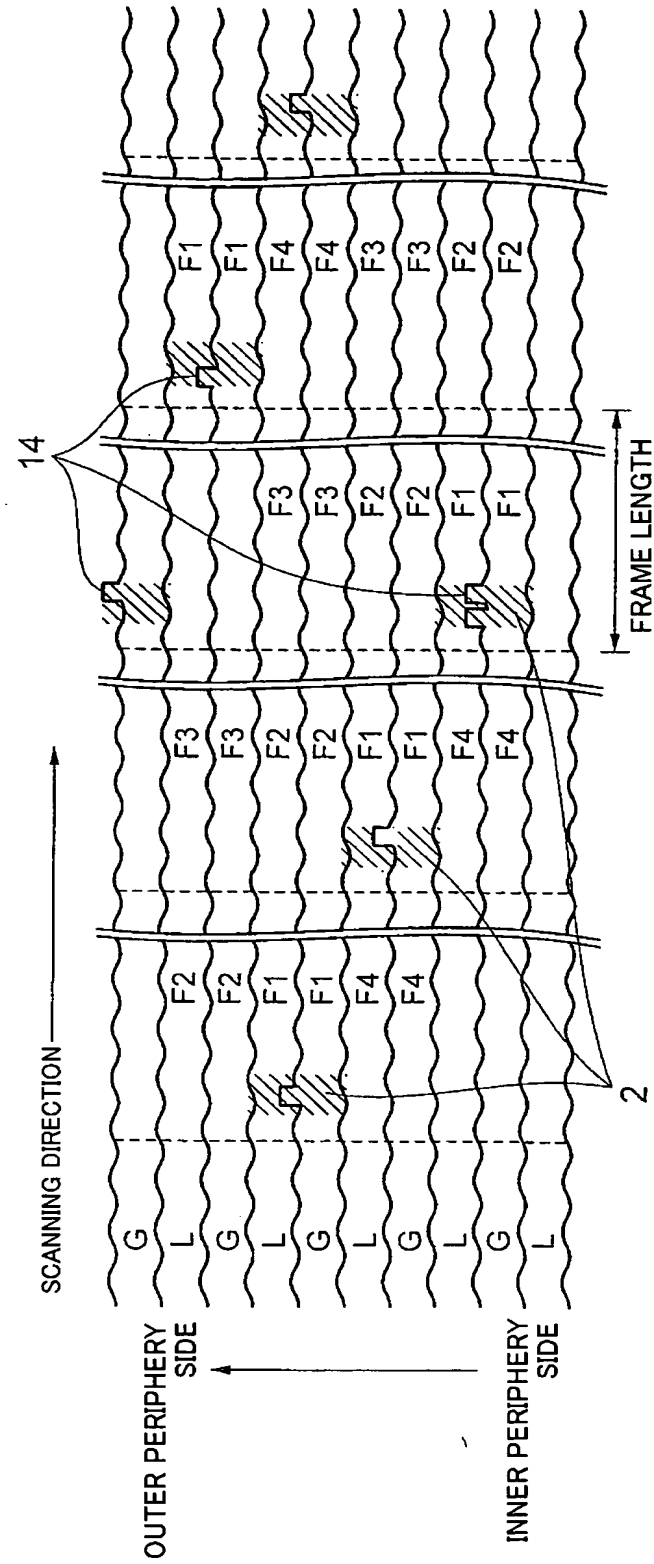
STATE	INPUT DATA	SUBSEQUENT DATA	MODULATION CODE	NEXT STATE
S0	00	XX	R01	S0
	01	XX	010	S0
	10	0X	010	S1
		1X,SY	R00	S1
	11	XX	R00	S0
S1	00	XX	001	S0
	01	XX	000	S0
	10	XX	001	S0
	11	XX	000	S0

FIG 8

STATE	SYNCHRONIZATION PATTERN (SY)	NEXT STATE
S0	R01001 000000 000000 000000 000001 00Y010	S0
	R00001 000000 000000 000000 000001 00Y010	S0
S1	001001 000000 000000 000000 000001 00Y010	S0
	000001 000000 000000 000000 000001 00Y010	S0

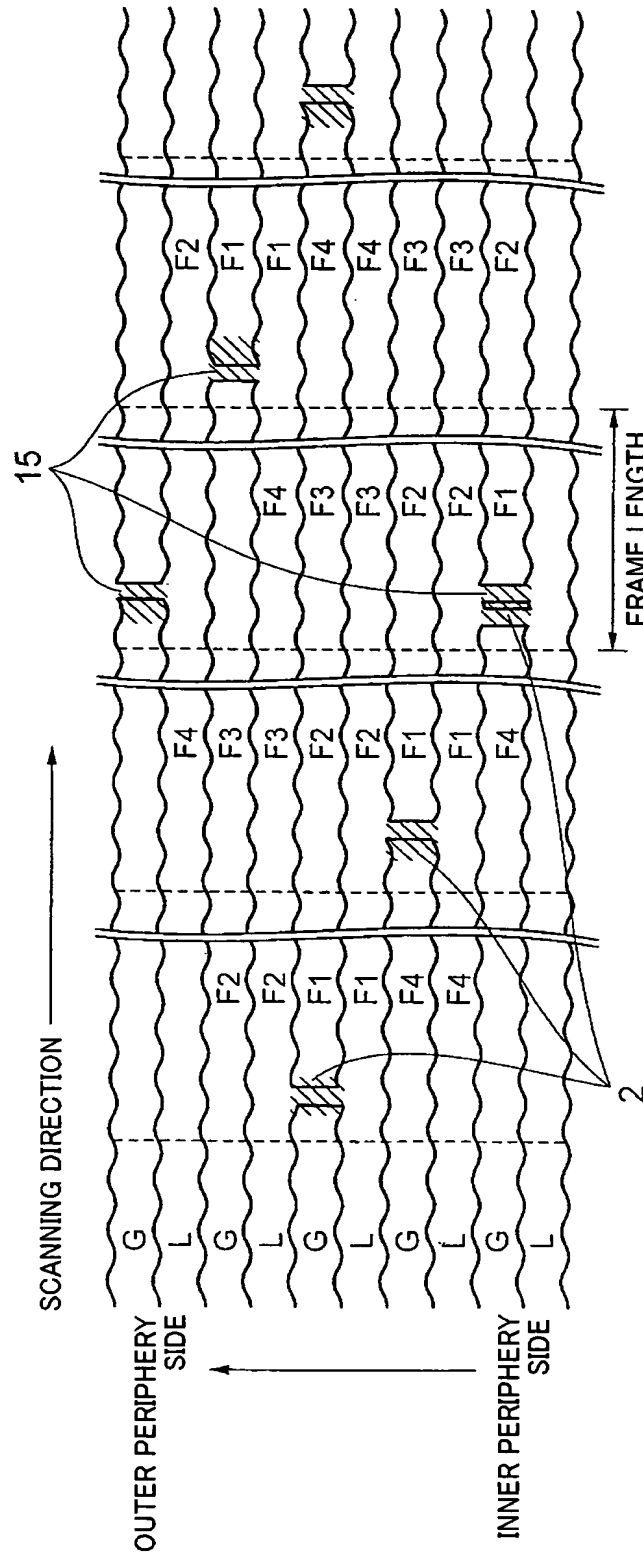
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FIG. 9



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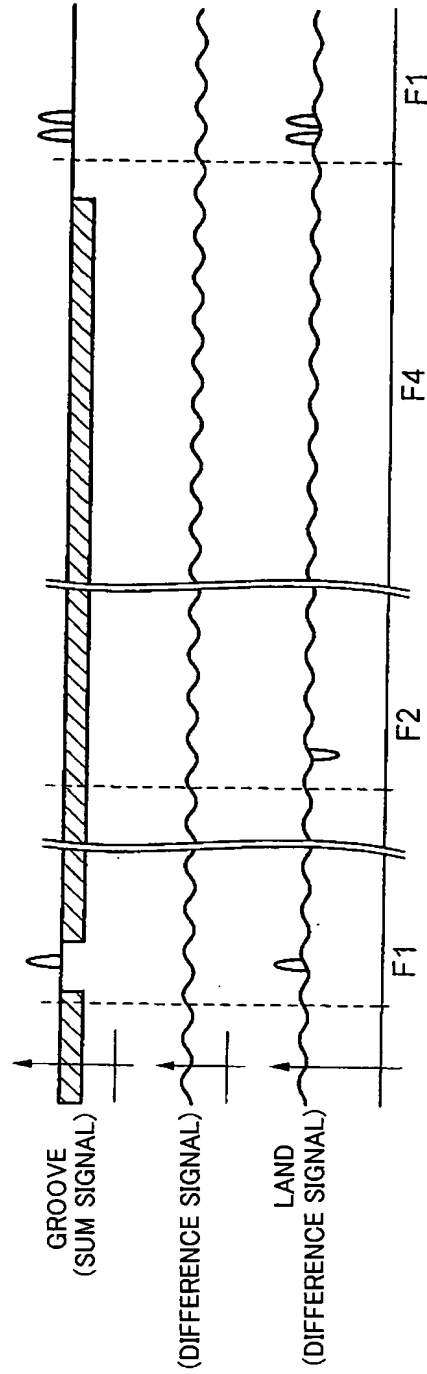
FIG. 10





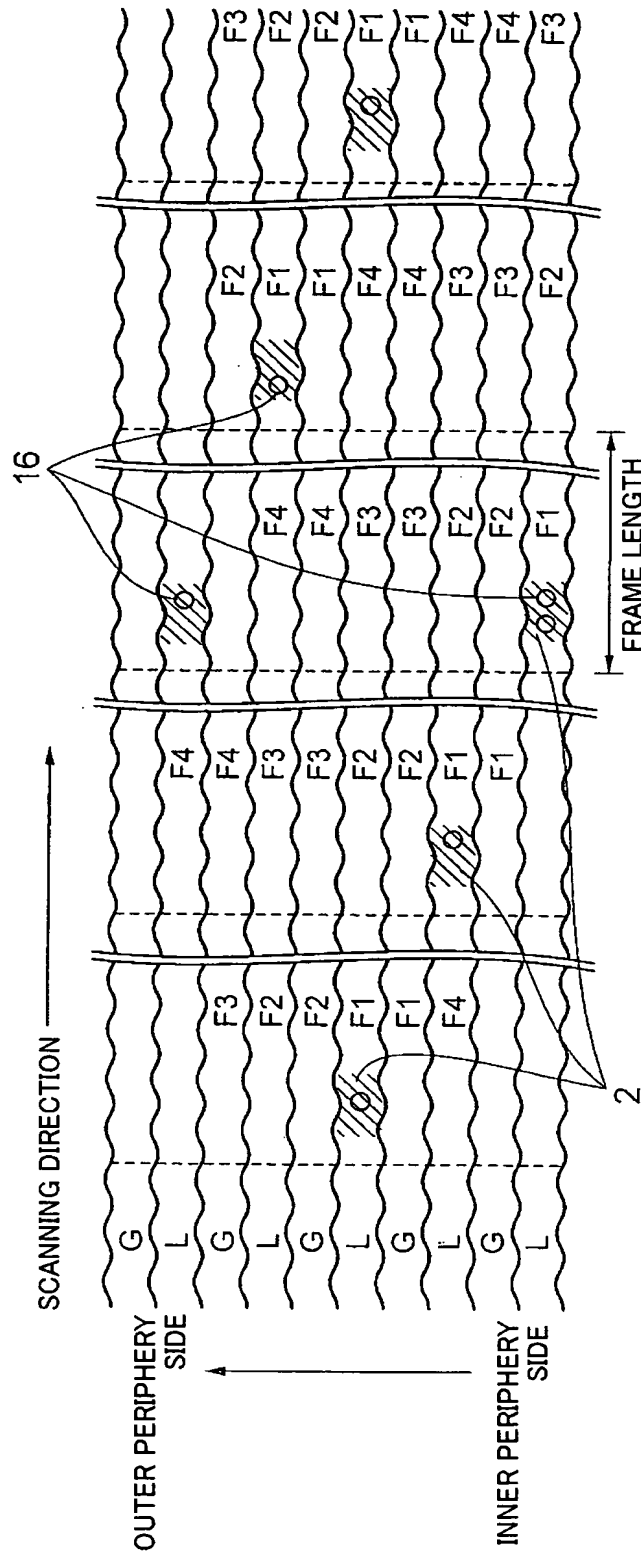
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FIG. 11



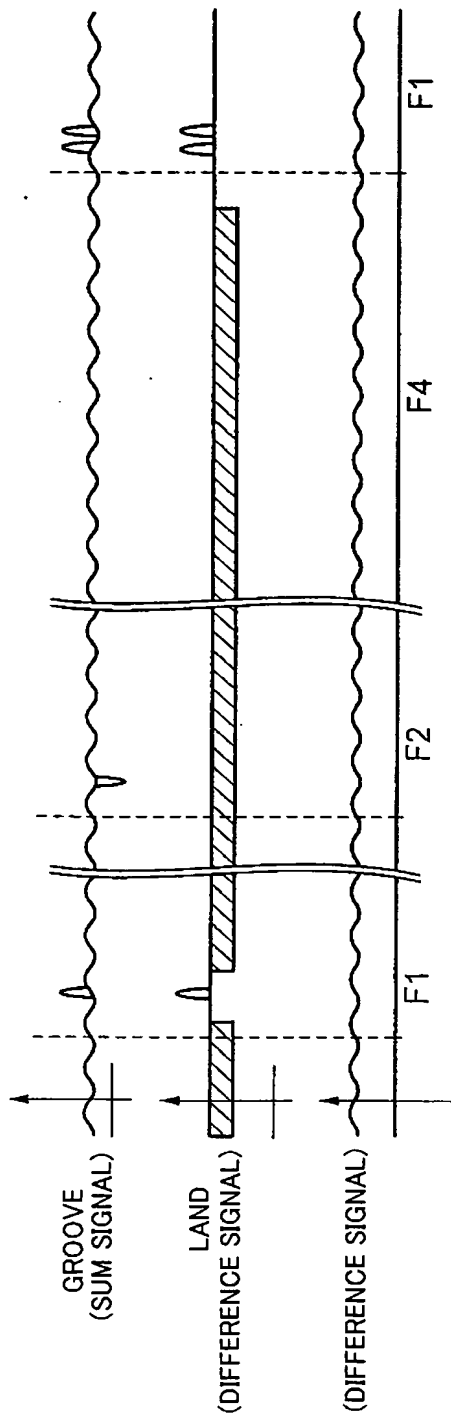
10/525098

FIG. 12



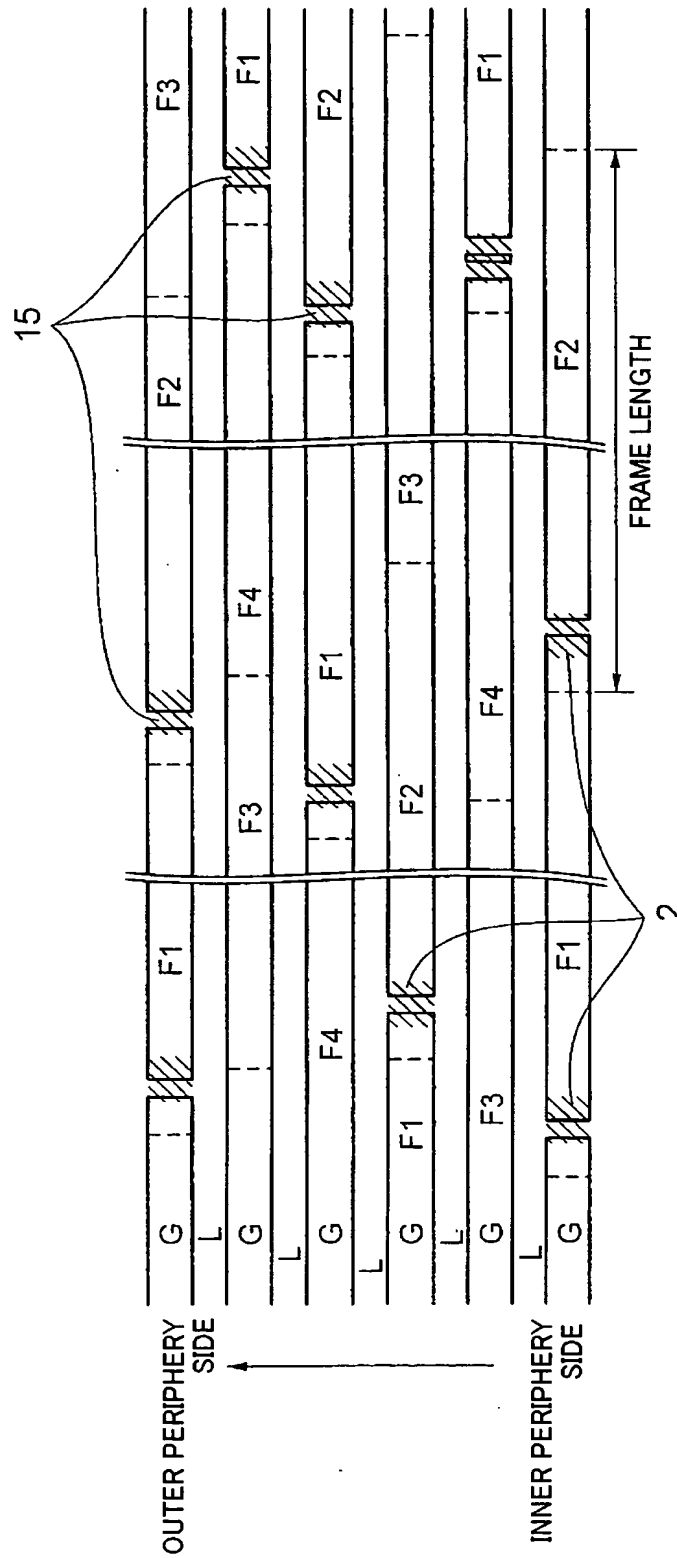
10/525098

FIG. 13



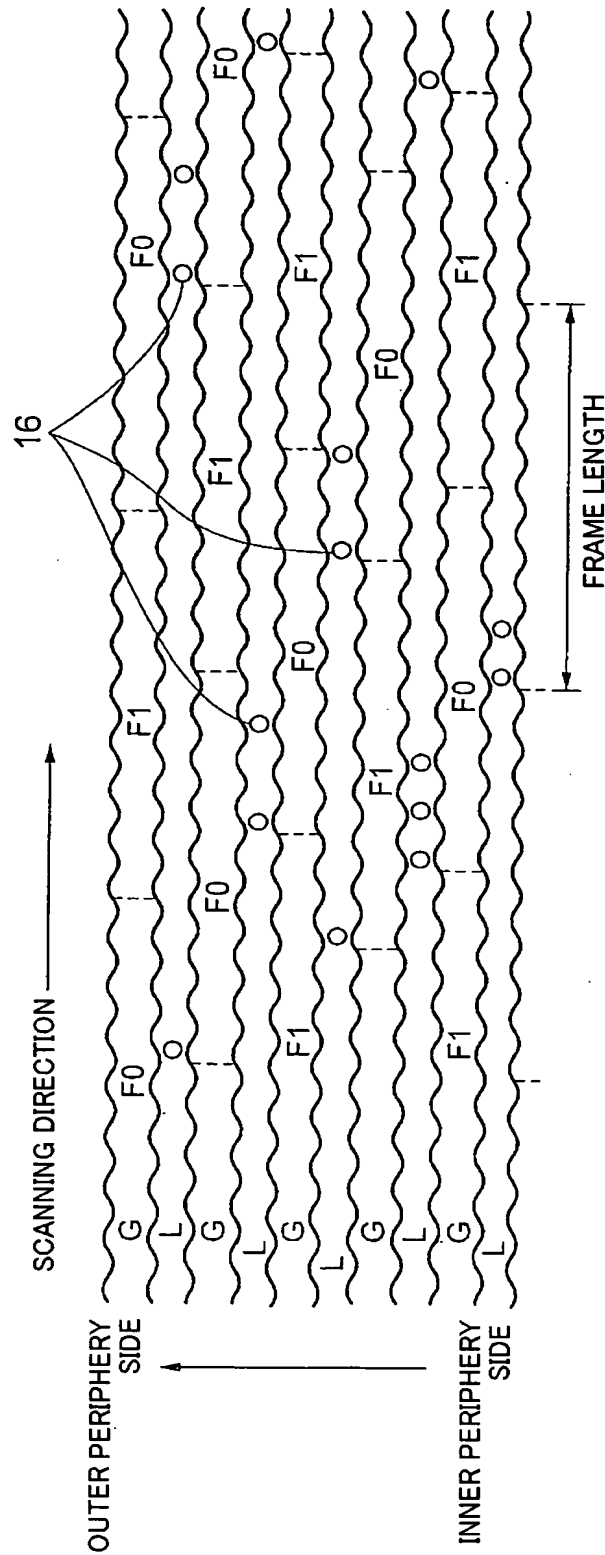
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FIG. 14



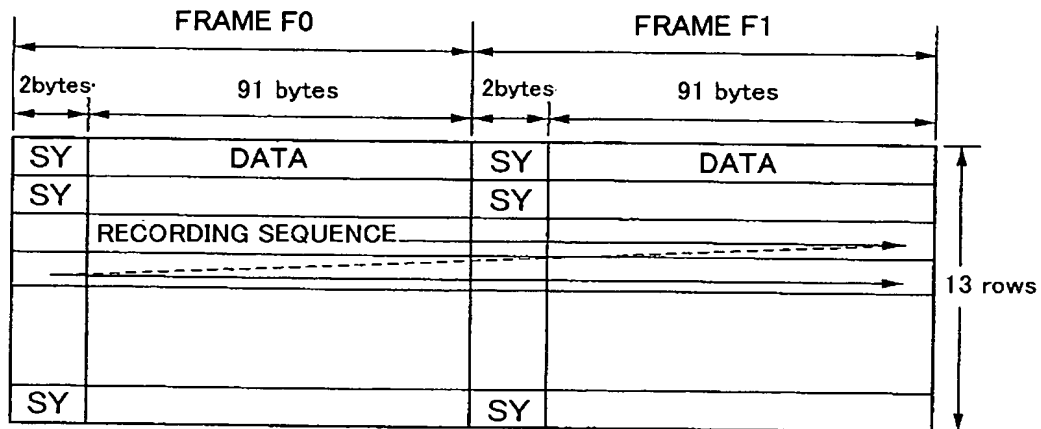
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FIG. 15



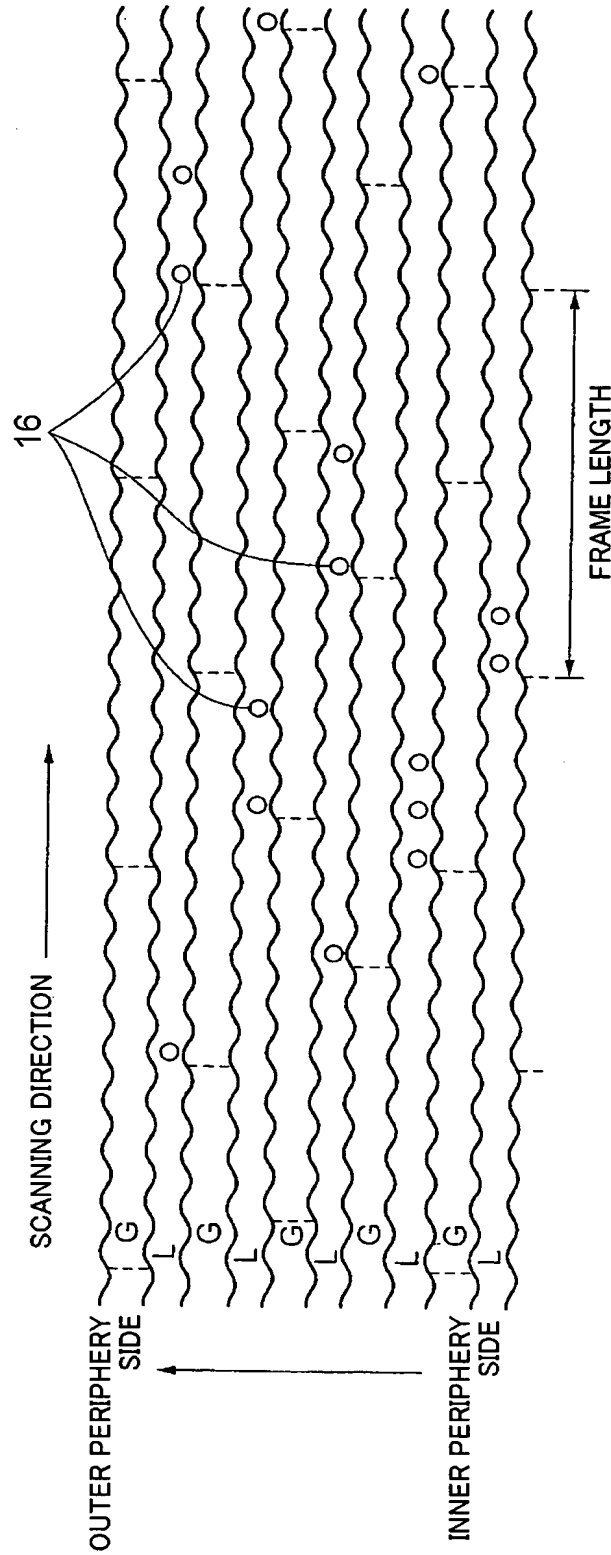
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FIG. 16



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FIG. 17



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FIG. 18

